

About NIT, Jalandhar

Dr B R Ambedkar National Institute of Technology was established in the year 1987 as Regional Engineering College and was given the status of National Institute of Technology (Deemed University) by the Government of India on October 17, 2002 under the aegis of Ministry of Human Resource Development, New Delhi. Now the Ministry of Human Resource Development, Government of India has declared the Institute as "Institute of National Importance" under the act of Parliament-2007. The institute is offering B Tech program in various disciplines like Computer Science and Engineering, Electronics and Communication Engineering, Instrumentation and Control Engineering, Mechanical Engineering, Civil Engineering, Textile Technology, Biotechnology, Chemical Engineering, and Industrial & Production Engineering. In addition to B Tech programs, the institute offers MBA, M Sc. programs in Physics, Chemistry and Mathematics, M Tech programs in all Engineering departments, and PhD programs.

Location of NITJ

The institute is located on Jalandhar-Amritsar NH-1 at a distance of 12km from Jalandhar Bus Stand, 10km from Jalandhar City Railway Station, and 16km from Jalandhar Cantt. Railway Station, 170 km from Chandigarh, 80 km from Raja Sansi International Airport, Amritsar and 375 km from Indira Gandhi International Airport, New Delhi. It is connected to New Delhi by major Rail/Road connection. The city is surrounded by famous rivers Sutlej and Beas. Jalandhar is internationally famous for sports goods, textile products and hand tools industry.

About Department of ECE

The Department of Electronics & Communication Engineering, Dr. B R Ambedkar NIT Jalandhar has been offering B.Tech programme in Electronics & Communication Engineering since 1989, M.Tech programme in Electronics & Communication Engineering (Full-Time) and VLSI Design (FullTime) from year 2007. The Department also offers Ph.D. programmes in various specializations, such as Biomedical Engineering, VLSI Design and Communication Systems etc. The course curriculum has been designed compatible to the existing and emerging needs of the industry. The Department has established state-of-the-art laboratories for B.Tech, M.Tech and Ph.D. research work. The Department has advanced Biomedical Signal Processing and Telemedicine Laboratory, VLSI Design Laboratory, Nanoelectronics Research Lab and Communication System Laboratory with sophisticated research equipment's. The department has an advanced VLSI lab sponsored by Ministry of Communication and IT under SMDP-II project, DST FIST Project and some other funded research projects.

About GWECA

GWECA holds the privilege of being first Government Engineering College for Women in the north India, established in April, 2007. With 2500 students in various bachelor and masters courses, the college is acting as a pedestal to explore the hidden talent for technical and managerial excellence. The college is located on NH-79 and is easily accessible through rail and road. The nearest airport is at Jaipur. Ajmer is the heart of the royal state of Rajasthan, situated in the South west of Jaipur, and wrapped through the greens of Arravali hills. Ajmer houses the Pushkar Teerth and tomb of 13th century Sufi Saint Khwaja Moinuddin Chisthy, which are uniformly valued by all.

Background

The current research in electronics and communication engineering has emerged as a very important technology in modern electronics featuring deep-sub micron manufacturing processes, low voltage operations, exploding speeds and smart programmable devices. The electronics industry worldwide is rapidly approaching another revolutionary leap in the global market scenario. This imparts the electronics industry a potential to create designs of incredible densities and lightning speeds while utilizing batteries to power them. This has had a phenomenal impact on widespread applications ranging from consumer electronics, communications, and defense to just about everything.

Objectives of the Short Term Course

The short terms course aims to include the following themes with particular emphasis to Biomedical Signal Processing, Advanced Communication Techniques, digital/analog VLSI Design, and Nanoelectronics. VLSI Circuit is a leading field to design of modern Electronic and Communication Systems. The main objective of this course is to provide the theoretical and practical knowledge of the current avenues of research in electronics and Communication Engineering. During this programme the academicians and industry personnel in their respective areas of expertise will discuss the current scenario on electronics and Communication engineering. In depth knowledge of upcoming trends in communication systems will also be delivered by experts. This course gives an opportunity for hands-on experience on the various tools

Aims of the STC

1. To enhance technical and professional competency of the faculty members, PG & PhD Scholar.
2. To promote interaction among the professionals working In Biomedical Signal Processing, Communication Systems, Microelectronics & VLSI Design.

Short Term Course

On

Current Avenues of Research in Electronics & Communication Engineering

(Under TEQIP-III)

(March 25th-to-29th, 2019)

Organized by

Department of Electronics & Communication
Engineering, NITJ

(Coordinators)

Dr. B.S Saini

Dr. Balwinder Raj

Coordinator (Mentee Institute)
Dr. S. K Agrawal (GWEC Ajmer)



Dr B R Ambedkar
National Institute of Technology, Jalandhar
(Punjab)-144011

Website: www.nitj.ac.in

In association with

Govt. Women Engineering College, Ajmer
(Rajasthan)-305002

Website: www.gweca.ac.in

Resource Persons

The resource persons for the course will be from IIT's, NIT's, Universities, reputed educational and research organizations.

Participants

There is no course fee. The course can be attended by the M.Tech/PhD Scholars, faculty members of an Engineering Institution and the delegates from various industries and research organization. The interested persons need to apply through proper channel to the Course Coordinator in the prescribed format by due date.

General Information

- The candidate will be informed of his/her selection in advance.
- Shortlisted candidates will be called for the course.
- Number of seats are limited.
- **No Course fee will be charged from the participants.**
- Boarding and lodging to the participants will be provided in the Institute Hostel on sharing basis at nominal charges.
- No TA/DA shall be paid to the participants for attending the course.
- **Last date of receiving the filled and signed registration forms: 15.03.2019.**

Important Dates

- Reporting date and time at the Institute: 25.03.2019/9:30AM.
- Venue: ECE Dept., NIT Jalandhar.

For Participants Registration

The complete registration form should reach by speed post or email to following address.

Corresponding address:

Dr. Balwinder raj

Course Coordinator and Assistant Professor
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Fax: 0181-2690320,2690932
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balwinderraj@gmail.com

Organizing Committee

Patron

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Director, Dr B R Ambedkar National Institute of
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Coordinator

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Coordinator (Mentee Institute)

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Organizing Team

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Dr. Arun Khosla
Dr. Mamta Khosla
Dr. Ramesh K Sunkaria
Dr. Indu Saini
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Dr. Ashish Raman
Dr. Deepti Kakkar
Dr. Deepak Bharti
Dr. Nitesh Kashyap
Dr. Sateesh Kumar Awasthi
Dr. Pawan Kumar Verma
Dr. Sukhwinder Singh

Registration Form for **One-Week Short Term Course** **On**

Current Avenues of Research in **Electronics & Communication** **Engineering**

(Under TEQIP-II)

(March 25th-to-29th, 2019)
ECE, NIT Jalandhar

Name: Mr./Ms/Dr. _____

Designation: _____

Academic Qualification _____

Specialization: _____

Teaching Experience: _____

Department: _____

Organization: _____

Address for Correspondence: _____

Accommodation Required: _____ Yes/No

MobileNo: _____

_____ E-

mail ID: _____

Signature and Date _____

RECOMMENDATION OF THE SPONSORING **AUTHORITY:**

The applicant is here by sponsored and will be
permitted to attend the STC, if selected.

Date: __Signature and Seal of
Sponsoring Authority