Govt. Women Engineering College, Ajmer

Dept. of CSE

1st Mid Term examination-2017-18

Sub:-ESD   **Duration:-1Hr**   **Max. Marks:-20**

Note:- Answer any two full question.

1(A) Define embedded system. Also classify embedded systems.   [2M+5M]
  (B) With suitable diagram discuss RAM memory.   [3M]

2(A) Discuss the embedded hardware in detail.   [7M]
  (B) Define and explain the Tri state (Devices) buffers.   [3M]

3(A) Briefly discuss the following:- ROM, PROM, EPROM.   [5M]
  (B) With neat diagram explain the architecture of a simple embedded system.   [5M]

Solution:-

1(A) Students can refer the NPTEL video: https://youtu.be/y9RAhEfLfJs
        For classification of Embedded system the book Embedded System by Rajkamal may be referred
1(B) Refer the book Embedded System Design: A Unified Hardware/Software Approach by
        Frank Vahid and Tony Givargis.
2(A) Refer the book Embedded System by Rajkamal.
2(B) Url may be followed: www.electronics-tutorial.ws/logic/logic-9.html
3(A) Refer the book Embedded System Design: A Unified Hardware/Software Approach by
        Frank Vahid and Tony Givargis.
3(B) Refer the NPTEL video: https://youtu.be/y9RAhEfLfJs