INVITATION FOR QUOTATION

TEQIP-III/2018/gwec/Shopping/8 **26-July-2018**

To,

Sub: Invitation for Quotations for supply of Goods for Digital Electronics Lab (ECE).

Dear Sir,

1. You are invited to submit your most competitive quotation for the following goods with item wise detailed specifications given at Annexure I,

Sr. No	Brief Description	Quantity	Delivery Period(In days)	Place of Delivery	Installation Requirement (if any)
1	"SOP and POS	5	30	Electronics and Communication Department	Yes
2	4-bit ripple adder/subtractor	5	30	Electronics and Communication Department	Yes
3	Asynchronous counter	5	30	Electronics and Communication Department	Yes
4	Counters and shift registers kit	5	30	Electronics and Communication Department	Yes
5	Digital Trainer Kit (Analog-Digital Circuits Development Platform)	10	30	Electronics and Communication Department	Yes
6	Multiplexer & Demultiplexer kit	5	30	Electronics and Communication Department	Yes
7	Universal IC Tester	2	30	Electronics and	Yes

				Communication Department	
8	Various type of flip flop	5	30	Electronics and Communication Department	Yes

2. Government of India has received a credit from the International Development Association (IDA) towards the cost of the **Technical Education Quality Improvement Programme[TEQIP]-Phase III** Project and intends to apply part of the proceeds of this credit to eligible payments under the contract for which this invitation for quotations is issued.
3. Quotation,
   3.1 The contract shall be for the full quantity as described above.
   3.2 Corrections, if any, shall be made by crossing out, initialing, dating and re writing.
   3.3 All duties and other levies payable by the supplier under the contract shall be included in the unit price.
   3.4 Applicable taxes shall be quoted separately for all items.
   3.5 The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.
   3.6 The Prices should be quoted in Indian Rupees only.
4. Each bidder shall submit only one quotation.
5. Quotation shall remain valid for a period not less than **45** days after the last date of quotation submission.
6. Evaluation of Quotations,
   The Purchaser will evaluate and compare the quotations determined to be substantially responsive i.e. which
   6.1 are properly signed ; and
   6.2 confirm to the terms and conditions, and specifications.
7. The Quotations would be evaluated for all items together.
8. Award of contract:
   The Purchaser will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.

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8.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.

8.2 The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.

9. Payment shall be made in Indian Rupees as follows:

   Delivery and Installation : 90% of total cost

   Satisfactory Acceptance : 10% of total cost

10. All supplied items are under warranty of **36** months from the date of successful acceptance of items.
11. You are requested to provide your offer latest by **15:00** hours on **25-Aug-2018 .**
12. Detailed specifications of the items are at Annexure I.
13. Training Clause (if any) **Required On Site**
14. Testing/Installation Clause (if any) **Required On Site**
15. Information brochures/ Product catalogue, if any must be accompanied with the quotation clearly indicating the model quoted for.
16. Sealed quotation to be submitted/ delivered at the address mentioned below,
    Makhupura, Nasirabad Road, Ajmer -305002
17. We look forward to receiving your quotation and thank you for your interest in this project.
18. Please write lab and package name on envelope.
19. You are requested to provide the company details viz. Firm Registration Certificate , GST Registration Certificate and any other necessary documents duly certified by Chartered Accountant and Notary Public.

(Authorized Signatory)

Name & Designation :

Govt. Women Engg. College
Ajmer

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AJMER

Annexure I

Sr. No	Item Name	Specifications
1	"SOP and POS	1) Study of Boolean function in different canonical forms. 2) Study of Quine Mclusky method and realize its NOR-OR implementation. 3) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected. 4) SPDT Switch for input logic section 5) Mains ON/OFF switch, Fuse and Jewel light. 6) LEDs for visual indication of output status of each 7) Mains ON/OFF switch, Fuse and Jewel light. 8) The unit is operative on 230V ±10% at 50Hz A.C. Mains. 9) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 10) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 11) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,
2	4-bit ripple adder/subtractor	1) 4-Bit ripple adder/subtractor using basic Half adder/subtractor & basic Full adder/Subtractor 2) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected. 3) SPDT Switch for input logic section 4) Mains ON/OFF switch, Fuse and Jewel light. 5) LEDs for visual indication of output status of each 6) Mains ON/OFF switch, Fuse and Jewel light. 7) The unit is operative on 230V ±10% at 50Hz A.C. Mains. 8) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 9) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 10) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,
3	Asynchronous counter	1) Construct a divide by 2,4, & 8 asynchronous counter 2) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected.

		3) SPDT Switch for input logic section 4) Mains ON/OFF switch, Fuse and Jewel light. 5) LEDs for visual indication of output status of each 6) flip-flop. Adequate no. of other Electronic Components. 7) Mains ON/OFF switch, Fuse and Jewel light. 8) The unit is operative on 230V ±10% at 50Hz A.C. Mains. 9) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 10) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 11) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,
4	Counters and shift registers kit	1) To Design - a) 4-Bit binary counter and ring counter using D Flip Flop b)Shift registers 2) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected. 3) SPDT switches for logic selection 4) Two pulsar switches for clear and clock arrangement. 5) LEDs for visual indication of output status of each 6) flip-flop. Adequate no. of other Electronic Components. 7) Mains ON/OFF switch, Fuse and Jewel light. 8) The unit is operative on 230V ±10% at 50Hz A.C. Mains. 9) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 10) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 11) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,
5	Digital Trainer Kit (Analog-Digital Circuits Development Platform)	1) Size of Breadboard : 172.5 mm x 128.5 mm 2) Connections on Breadboard . 1685 3) DC Power Supply : +5 V; l A, -5 V; 500 mA, +3 V to +15 V, 500 mA(variable) -3 V to -15 V; 500 mA (variable) 4) Pulsar Generator : 1Hz to 1MHz in 6 steps Variable in between steps

		5) Amplitude : 3 V-15 V (CMOS), 5V (TTL) Duty cycle: 50 %, TTL/CMOS output 6) Pulsar Switches : 2 nos. (Push to 'On') 7) Data Switches : 16 nos (Toggle switches for both TTL & CMOS mode) 8) LED Display : 8 nos (TTL/ CMOS Mode) 9) BCD to Seven Segment Display : 2 nos 10) Logic Probe : Logic level indicator for TTL/ CMOS (7 Segment) 11) Power Supply : 110-220 V ± 10%, 50/60 Hz 12) Weight : 3 Kgs. Approximately 13) Dimensions (mm) : W 326 x H 52 x D 252 14) Learning material : CD (Theory, procedure, reference results, etc). **The software should meet following Key Features:** The Board should comprise with Online Simulation, Teaching & Learning Software on Digital Electronics. The software should introduce the fundamental concepts of digital systems which should illustrate by various animations and relevant examples for quick understanding of students along with the simplified theory. At the end of each topic relevant quizzes & FAQs should also be given for the students to self estimate their understanding. It should cover following key topics like Digital number system, Boolean Algebra & Logic Circuits, Digital Logic Gates, Simplification of Boolean Functions, Digital Combinational Logic, Combinational Arithmetic Circuits, Sequential Circuits, Digital Logic Families, adder & sub tractor, Multi vibrators, Schmitt trigger, Logic Families, Shift Registers & Counters Semiconductor Memories, Analog to Digital & Digital to Analog converters.
6	Multiplexer & Demultiplexer kit	1) a)4:1 Multiplexer and 1:4 Demultiplexer using basic gates b)8:1 Multiplexer and 1:8 Demultiplexer using blocks of 4:1 Multiplexer and 1:4 Demultiplexer 2) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected. 3) SPDT Switch for input logic section 4) Mains ON/OFF switch, Fuse and Jewel light. 5) LEDs for visual indication of output status of each 6) Mains ON/OFF switch, Fuse and Jewel light. 7) The unit is operative on 230V ±10% at 50Hz A.C. Mains.

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		8) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 9) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 10) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,
7	Universal IC Tester	1) Tests a wide range of Digital IC's such as 74 Series, 40/45 Series of 2) CMOS IC's 3) It can test Microprocessor 8085, 8086, Z80, 8051, 89c51 4) It tests Peripherals like 8255, 8279, 8253, 8259, 8251, 8155, 5) 6264,62256,8288,8284 6) It tests Opamp,555, Transistor Arrays, Analog switches, Opto couplers and Others 7) It tests 7 segment display of common cathode & common anode type 8) It has Auto search facility of IC's 9) Test by: Truth table/sequence table comparison 10) ZIF: 40 pin DIP ZIF sockets 11) Keys: 28 cherry keys Key pad with numerical & functional keys 12) Display: 9 Digit Seven Segment Display 13) Supply Input Voltage: 230V AC
8	Various type of flip flop	1) R-S,J-K and D-Flip flop with and without clock signal and verify their truth table 2) + 5V D.C. at 1Amp, IC Regulated Power Supply Internally connected. 3) SPDT Switch for input logic section 4) Mains ON/OFF switch, Fuse and Jewel light. 5) LEDs for visual indication of output status of each 6) Mains ON/OFF switch, Fuse and Jewel light. 7) The unit is operative on 230V ±10% at 50Hz A.C. Mains. 8) Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ meter. 9) Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms. 10) Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures,

FORMAT FOR QUOTATION SUBMISSION

(In letterhead of the supplier with seal)

Date: _____

To:

_____

_____

Sl. No.	Description of goods (with full Specifications)	Qty.	Unit	Quoted Unit rate in Rs. (Including Ex Factory price, excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and warranty/ guaranty commitments)	Total Price (A)	Sales tax and other taxes payable	
						In %	In figures (B)
Total Cost							

Gross Total Cost (A+B): Rs. _____

We agree to supply the above goods in accordance with the technical specifications for a total contract price of Rs. ———————— (Amount in figures) (Rupees ————————amount in words) within the period specified in the Invitation for Quotations.

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We confirm that the normal commercial warranty/ guarantee of ———————— months shall apply to the offered items and we also confirm to agree with terms and conditions as mentioned in the Invitation Letter.

We hereby certify that we have taken steps to ensure that no person acting for us or on our behalf will engage in bribery.

Signature of Supplier

Name: _____

Address: _____

Contact No: _____