

Government Women Engineering College, Ajmer, Makhupura, Nasirabad Road, Ajmer -305002

INVITATION LETTER

Package Code: TEQIP-III/2019/RJ/gwec/125	Current Date: 10-Aug-2019
Package Name: GWECA/ECE/E.E.D Lab	Method: Shopping Goods

To,		

Sub: INVITATION LETTER FOR GWECA/ECE/E.E.D Lab

Dear Sir,

1. You are invited to submit your most competitive quotation for the following goods with item wise detailed specifications given at Annexure I,

Sr. No	Item Name	Quantity	Place of Delivery	Installation Requirement (if any)
1	Analog Development Board	2	ECE, GWEC Ajmer	Yes
2	Digital storage Oscillosope	1	ECE, GWEC Ajmer	Yes
3	FPAA Board (Field Programmable Analogic Design Board)	2	ECE, GWEC Ajmer	Yes

2. Government of India has received a credit from the International Development Association (IDA) towards the cost of the Technical Education Quality Improvement Programme [TEQIP]-Phase III Project and intends to apply part of the proceeds of this credit to eligible payments under the contract for which this invitation for quotations is issued.

3. Quotation

- 3.1 The contract shall be for the full quantity as described above.
- 3.2 Corrections, if any, shall be made by crossing out, initialling, dating and re writing.
- 3.3 All duties and other levies payable by the supplier under the contract shall be included in the unit Price.

- 3.4 Applicable taxes shall be quoted separately for all items.
- 3.5 The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.
- 3.6 The Prices should be quoted in Indian Rupees only.
- 4. Each bidder shall submit only one quotation and the bidder shall submit the Technical Bid and Financial Bids in separate sealed covers, clearly super-scribing "Technical bid for E.E.D. Lab" and "Financial bid for providing E.E.D. Lab", respectively. These two sealed covers shall be put in another cover which should also be sealed, signed and duly super-scribed "Tender for providing E.E.D. Lab with Package Code".
- Quotation shall remain valid for a period not less than 90 days after the last date of quotation submission.
- **6.** Evaluation of Quotations: The Purchaser will evaluate and compare the quotations determined to be Substantially responsive i.e. which
 - 6.1 are properly signed; and
 - 6.2 Confirm to the terms and conditions, and specifications.
- 7. The Quotations would be evaluated for all items together.
- 8. Award of contract The Purchaser will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.
 - 8.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of Contract.
 - 8.2 The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be Incorporated in the purchase order.
- 9. Payment shall be made in Indian Rupees as follows:

Payment Description	Expected Delivery Period (in Days)	Payment Percentage
Satisfactory Acceptance	30	10
Satisfactory Delivery & Installation	30	90

- Liquidated Damages will be applied as per the below: Liquidated Damages Per Day Min %:0.10 Liquidated Damages Max %:10
- 11. All supplied items are under warranty of **36** months from the date of successful acceptance of items and AMC/Others is .
- 12. You are requested to provide your offer latest by 14:00 hours on 26-Aug-2019.
- 13. Detailed specifications of the items are at **Annexure I**.
- 14. Training Clause (if any) Yes
- 15. Testing/Installation Clause (if any) Yes
- 16. Performance Security shall be applicable: 0%
- 17. Original Information brochures/ Product catalogue, if any must be accompanied with the quotation clearly indicating the model quoted for.
- 18. Sealed quotation to be submitted/ delivered at the address mentioned below, **Government**Women Engineering College, Ajmer, Makhupura, Nasirabad Road, Ajmer -305002
- 19. The bidder must submit the company details viz. Firm Registration Certificate, GST Registration Certificate and any other necessary documents duly certified by Chartered Accountant / Notary Public (Audited balance sheets including profit and loss accounts for the three financial years viz. 2015-16, 2016-17 & 2017-2018) along with their bid.
- 20. The quotation would be opened on 26-Aug-2019 at 15:00 hrs at TEQIP-III Office, Govt. Women Engineering College Ajmer, Rajasthan 305002, India in the presence of bidder representative who choose to attend the opening. The bidder representative who is present shall sign an Attendance Sheet evidencing their attendance.
- 21. Only authorized dealer/ agency of Original Equipment Manufacturer (OEM) or OEM should apply against this invitation for bid. In the case of the bidder, offering to supply goods under the bid, which the bidder does not manufacture or otherwise produce, the bidder has to provide Manufacturer's Authorization Certificate strictly as per format at Annexure A. Bids submitted without authorization certificate as per Annexure A will be summarily rejected.
- 22. Notwithstanding the above, the Institute reserves the right to accept or reject any quotation(s) and to cancel the process and reject all quotation(s) at any time.

- 23. Dispute if any shall be subjected to the jurisdiction of Rajasthan in Ajmer/Jaipur.
- 24. We look forward to receiving your quotation and thank you for your interest in this project.

(Authorized Signatory)
Name & Designation

Annexure I

Sr. No	Item Name	Specifications
1	Analog Development Board	Analog Development Boards are used to provide student and engineers the platform for creating analog systems using modules such as OP-Amps, analog multipliers, Digital-to-analog converters, Low-dropout Voltage regulators, transistors, diodes and more. It is used to perform analog lab experiments and provides a cost efficient platform for students to realize almost any analog system using general purpose ICs such as OP-Amps and analog multipliers. Board consists of:
		Main Power The kit has screw terminals to connect +/-10V power supply. All the ICs on the board are internally Connected to power supply.
		DC-DC converter It is a non-synchronous buck converter which can be used to provide a resistor-selected 3.3V or 5V output, that delivers up to 2.5A from up to 16V input bus.
		LDO regulator Low dropout linear voltage regulator is capable of 200mA output current at 5V fixed output voltage level. It is a low quiescent current, low noise, high PSSR, fast start-up LDO.
		Transistor sockets Board comes with PNP and NPN general-purpose bipolar transistors as well as P-channel MOSFET transistor.
		Breadboard area General Purpose Area with 2.54mm/100mil pad spacing can be used to place additional components And expand set of exercises with new experiments. Analog Multipliers Three analog multipliers are included in the kit. These are wide-bandwidth . Each multiplier is a 14-pin IC and operates on internally provided+/-10V supply.
		Diodes It includes two diode sockets on the board, which can be used as rectifiers in custom laboratory.
		OP-Amps Type I Three OP-Amps are available on the board. Each IC

has two amplifiers which are labeled A and B. OP-Amps Type I can be connected in the inverting configuration only.

D/A converter

There are two digital-to-analog converters (DAC) provided in the kit. They are 12-bit, parallel-input multiplying DACs.

OP-Amps Type III

Three OP-Amps are available on the board. Each IC has two amplifiers which are labeled A and B. OP-Amps Type III can be used as voltage buffers

OP-Amps Type II

Three OP-Amps are available on the board. Each IC has two amplifiers which are labeled A and B. OP-Amps Type II can be configured to act as inverting or non-inverting

Trimmers

There are two 1k_ trimmers in the kit to enable the designer to obtain a variable voltage if needed for a circuit. These operate respectively in range from 0V to +10V, and -10V to 0V

2

Should be capable to obtain the waveform on Digital Storage Oscilloscope having Technical Specification-Bandwidth: 50 MHz

Number of Channel :2 Analog Channel

Sampling Rate :1 GS/s all Channels Time Base Range :5ns/div to 50 sec/div

Memory Depth :100kpts

Acquisition Rate: = 50,000 per second Coupling: AC,

DC Input Impedance :1 MO ± 2%/16 pF ±3 pF Vertical Sensitivity :500µV/Div to 10 V/div

Vertical Resolution :8 Bits Display : =7 inch Trigger ion : Edge, pulse width, video, pattern/state Digital Voltmeter and Frequency Counter:

Digital voltmeter and 5 - digit frequency counter upto scope bandwidth should be available

Automatic Measurement : More than 20+ automatic measurement function should be available alongwith math function, FFT plot with dBV Vs frequency should be available

Training Signal : Different types of training signal should be available built - in with lab guide, tutorials and content on Oscilloscope fundamentals

Operating Temperature: Operating: 0 to 50° C

Passive Probes: 2 Passive Probe should be provided with oscilloscope and have able 10:1 and 1:1 attenuation.

Digital storage Oscillosope

		Serial Decodes (Optional): I ² C and UART Option should be available for future upgradable(Optional) PC Connectivity and Software: USB connectivity and software should have feature to control & visualize multiple measurement simultaneously and data logging upto 1-hour.
3	FPAA Board (Field Programmable Analogic Design Board)	 Analog design development board. Dynamically reconfigurable through plug & play USB port Compatible to Designer software with USB interface for downloading of design. Static reconfiguration with SPI EPROM. Directly programs SPI PROM from USB port. Analog interface blocks (7 nos) for level-shifting, attenuating, filtering, etc. 4 dedicated input or output blocks with separate input & output ports. 3 pairs of input or output blocks with jumper ion for direction. High speed/precision OPAMPs used for signal conversion for every input/output block. Terminal screw headers for easy interface. Onboard microphone with amplifier. Microphone output header for amplified output. Onboard analog function generator Sine wave @ 1Vpp Onboard potentiometer to vary frequency from Hz to KHz range Facility to change passive components for varying frequency ranges. Configuration reset button. Onboard PIC microcontroller for programming and PC interface. LED indication for successful programming & error. LED indication for power supply. On-board 16-MHz oscillator module. Facility to change frequency ranges easily. USB powered with optional ion for external power supply. On board regulators for 3.3V and 5V supply with PCB as heat sink. Small & ergonomic design of board. Comes in attractive enclosure. Printed user manual o Complete reference manual for using board with chapters on getting started, installing USB drivers, using design software, developing VC++ prototypes, etc CD-ROM with drivers, software, PROM programmer and lots of ready to use examples.

MANUFACTURER AUTHORIZATION FORM

	No	dated		
То				
Dear Sir:				
	Package No	D	<u>-</u>	
		(Nam		
description of good factory registra M/s	ds offered) having f tion no	factories at (<i>Name and add</i> s manufactured by us a	(address of address of Agent) to sub-	of factory) with y authorize omit a bid, and
•		anty as per your invitat e firm against this Invit	•	ls and
			Yours faithfull	y,
			(Name)	
			(Name of manufac	turers)

Note: This letter of authority should be on the letterhead of the manufacturer or OEM and should be signed by a person competent and having the power of attorney to legally bind the manufacturer.

FORMAT FOR QUOTATION SUBMISSION

(In letterhead of the supplier with seal)

Date:							
To:							
SI. No.	Description of goods \ (with full Specifications)	Qty.	Unit	Quoted Unit rate in Rs. (Including Ex-Factory price, excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and warranty/ guaranty commitments)	Total Price (A)	Sales tax and	other taxes payable In figures (B)
			Total C	ost			
(Rupees — We confirm terms and We hereby Signature Name: Address: _	an that the normal conditions as ment	nount i comme ioned i e take	n words rcial wa n the In	cordance with the technical specification within the period specified in the Invitation rranty/ guarantee of ——————————————————————————————————	ition for Quotations. nonths shall apply t	o the offered items and	