

Course Summary

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I attended one week Short-Term Train Programme on "Advanced Digital Devices and Systems" at Department of Electronics and Communication Engineering in Malaviya National Institute of Technology, Jaipur.

In Inaugural Session Director of MNI Jaipur has delivered significant lecture on

teaching profession. In this five days STTP the highlights were as follows, by different eminent experts:

- FPGA Architecture
- Low Power Design
- Fixed and Floating Point Representations
- Digital Interconnects and Related Issues
- State of art memory design
- FPGA based Design
- Hands on experimentation for FPGA based Implementations
- FPGA Interfacing with principles verification strategies.

Shri
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